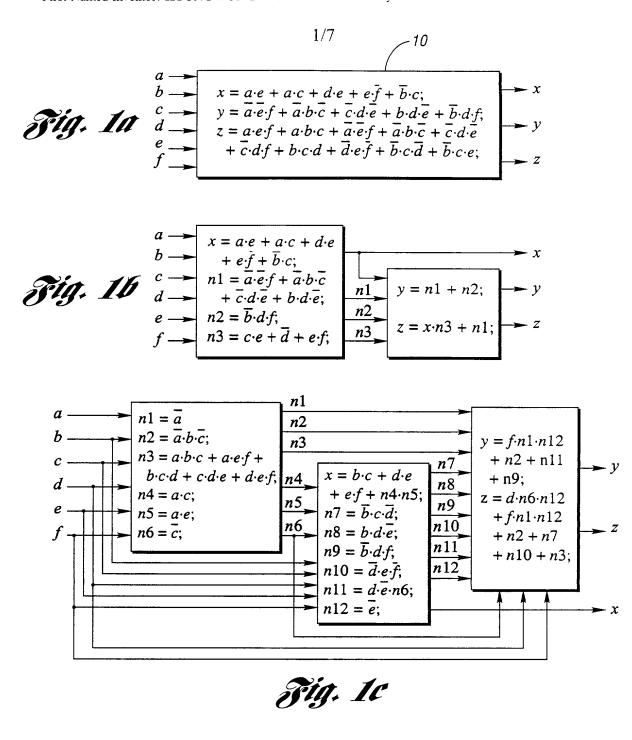
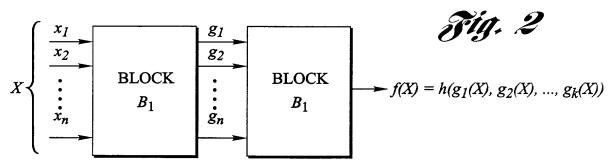
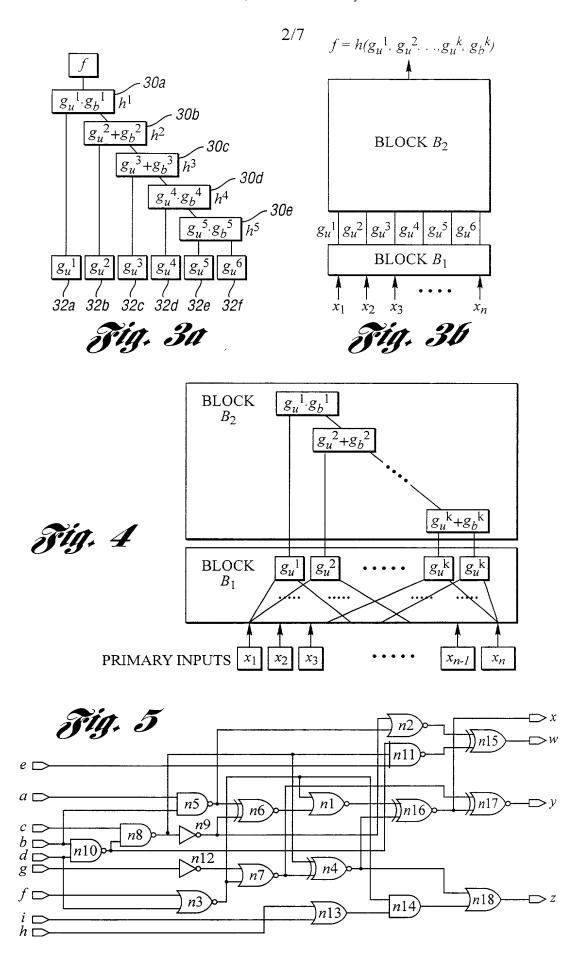
Title: METHOD AND SYSTEM FOR SYNTHESIZING A CIRCUIT REPRESENTATION INTO A NEW CIRCUIT REPRESENTATION HAVING GREATER UNATENESS First Named Inventor: HYUNGWON KIM. Ph.D. et al. / Atty. Docket No 'UOM 0209 PUSP

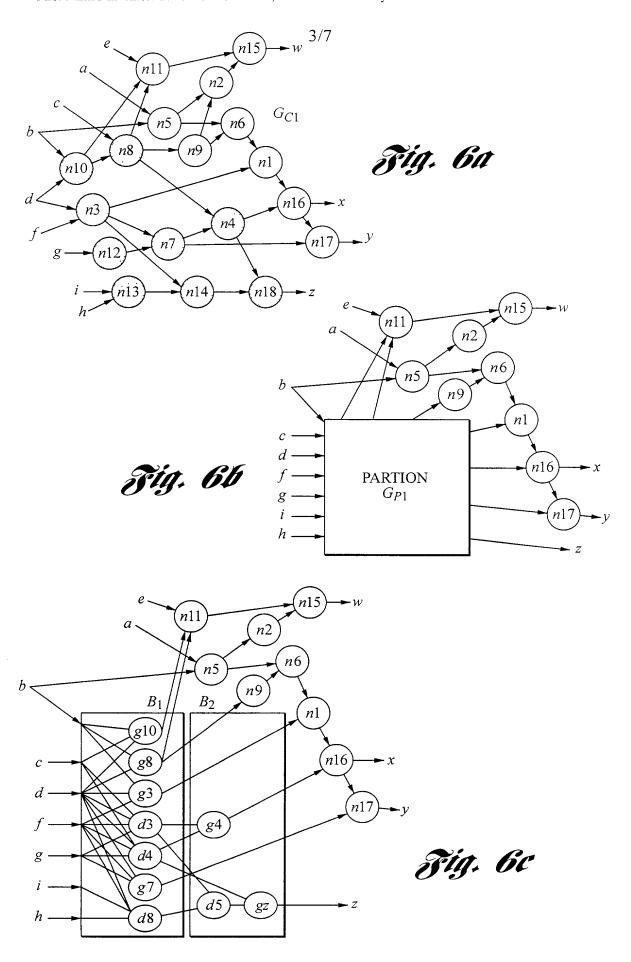




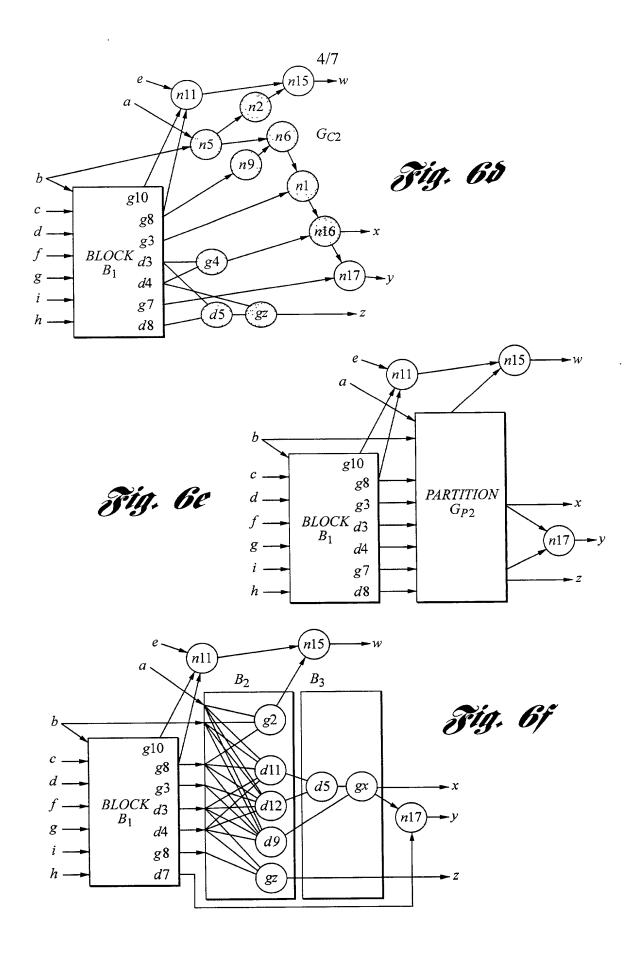
## Title: METHOD AND SYSTEM FOR SYNTHESIZING A CIRCUIT REPRESENTATION INTO A NEW CIRCUIT REPRESENTATION HAVING GREATER UNATENESS First Named Inventor: HYUNGWON KIM, Ph.D. et al. / Atty. Docket No. UOM 0209 PUSP



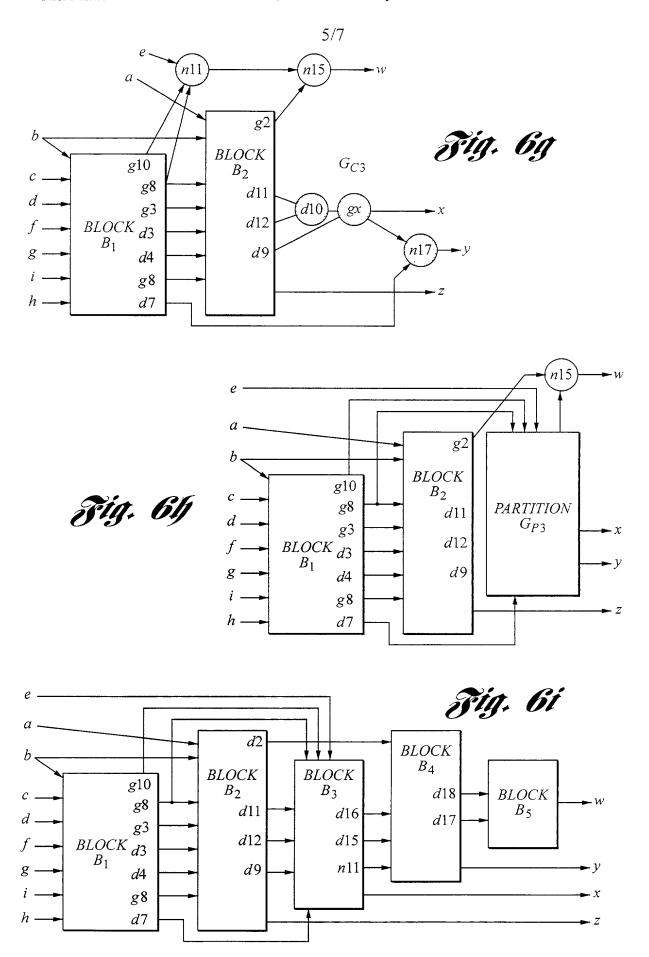
Title: METHOD AND SYSTEM FOR SYNTHESIZING A CIRCUIT REPRESENTATION INTO A NEW CIRCUIT REPRESENTATION HAVING GREATER UNATENESS
First Named Inventor: HYUNGWON KIM, Ph.D. et al. / Atty. Docket No.. UOM 0209 PUSP



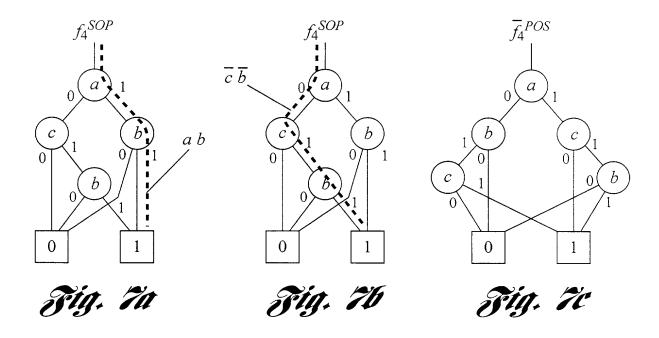
Title: METHOD AND SYSTEM FOR SYNTHESIZING A CIRCUIT REPRESENTATION INTO A NEW CIRCUIT REPRESENTATION HAVING GREATER UNATENESS
First Named Inventor: HYUNGWON KIM, Ph.D. et al. / Atty Docket No.: UOM 0209 PUSP



Title: METHOD AND SYSTEM FOR SYNTHESIZING A CIRCUIT REPRESENTATION INTO A NEW CIRCUIT REPRESENTATION HAVING GREATER UNATENESS
First Named Inventor: HYUNGWON KIM, Ph.D. et al. / Atty. Docket No UOM 0209 PUSP







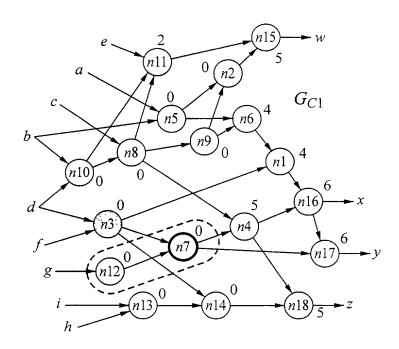
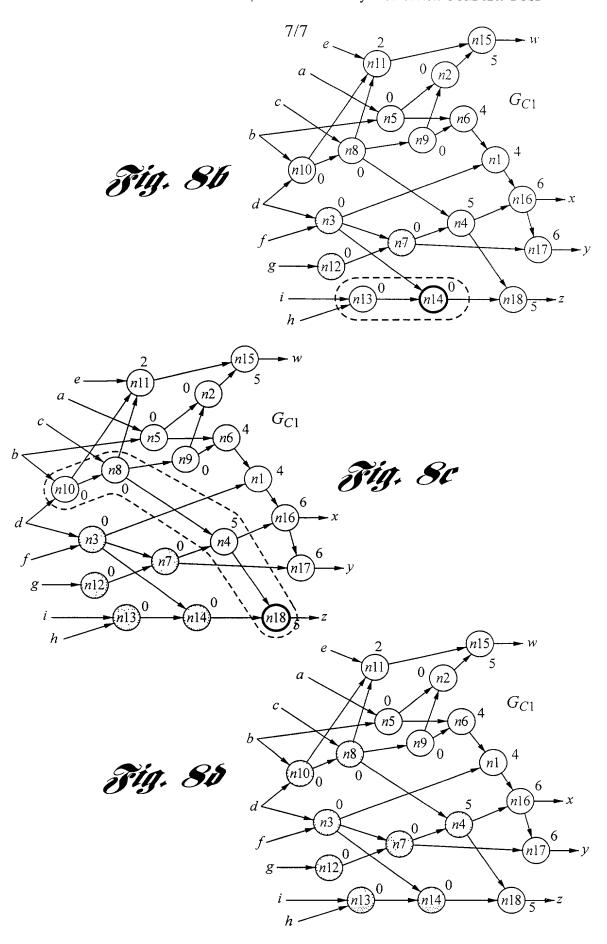


Fig. Sa

Title: METHOD AND SYSTEM FOR SYNTHESIZING A CIRCUIT REPRESENTATION INTO A NEW CIRCUIT REPRESENTATION HAVING GREATER UNATENESS
First Named Inventor: HYUNGWON KIM, Ph.D. et al. / Atty. Docket No.: UOM 0209 PUSP



1695